DEC 08 2005

FORM PTO-1449	ATTY. DOCKET NO. 1778.0000002 (0055.20US)	APPLICATION NO. 09/662,832
INFORMATION DISCLOSURE STATEMENT	APPLICANT Van Hook *et al.*	
	FILING DATE September 15, 2000	GROUP 2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
✓	AA1	3,654,621	04/1972	Bock *et al.*	340	172.5	
✓	AB1	3,916,388	10/1975	Shimp *et al.*	340	172.5	
✓	AC1	4,023,023	05/1977	Bourrez *et al.*	235	152	
✓	AD1	4,109,310	08/1978	England *et al.*	364	200	
✓	AE1	4,128,880	12/1978	Cray, Jr.	364	200	
✓	AF1	4,130,880	12/1978	Nutter	364	900	
✓	AG1	4,219,874	08/1980	Gusev *et al.*	364	200	
✓	AH1	4,317,170	02/1982	Wada *et al.*	364	200	
✓	AI1	4,396,982	08/1983	Wada *et al.*	364	200	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
✓	AJ1	WO 97/07450 A1	02/1997	PCT	G06F	7/00	N/A
✓	AK1	WO 97/08608 A1	03/1997	PCT	G06F	7/00	N/A
✓	AL1	0 681 236 A1	11/1995	EP	G06F	9/302	N/A
✓	AM1	0 681 236 B1	11/2000	EP	G06F	9/302	N/A

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

✓	AN	1	*An Introduction to Thumb*™, Version 2.0, Advanced RISC Machines, 29 pages (March 1995).
✓	AO	1	Bier, J., "DSP16xxx Targets Communications Apps," *Microprocessor Report*, MicroDesign Resources, pp. 11-15 (September 15, 1997).
✓	AP	1	Case, B., "Superscalar Techniques: SuperSPARC vs. 88110," *Microprocessor Report*, MicroDesign Resources, Vol. 5, No. 22, pp. 1 and 6-11 (December 4, 1991).
✓	AQ	1	Diefendorff, K., "88410 Second Level Cache," *Microprocessor Forum*, Motorola Inc., 20 pages (November 1991).
✓	AR	1	Diefendorff, K. and Allend, M., "Organization of the Motorola 88110: A Superscalar RISC Microprocessor," 12 pages, Source and date unknown.

EXAMINER	DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

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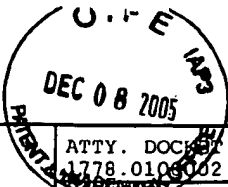
FORM PTO-1449

INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO. 1778.0109002 (0055.20US)	APPLICATION NO. 09/662,832
APPLICANT Van Hook et al.	
FILING DATE September 15, 2000	GROUP 2183

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
	AA2	4,491,910	01/1985	Caudel et al.	364	200	
	AB2	4,507,731	03/1985	Morrison	364	200	
	AC2	4,520,439	05/1985	Liepa	364	200	
	AD2	4,583,199	04/1986	Boothroyd et al.	364	900	
	AE2	4,773,006	09/1988	Kinoshita et al.	364	200	
	AF2	4,814,976	03/1989	Hansen et al.	364	200	
	AG2	5,150,290	09/1992	Hunt	364	736	
	AH2	5,418,915	05/1995	Matuda et al.	395	325	
	AI2	5,513,366	04/1996	Agarwal et al.	395	800	

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
	AJ2						Yes No
	AK2						Yes No
	AL2						Yes No
	AM2						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)

	AN	2	Fiegel, C.P., "TI Introduces Four-Processor DSP Chip," *Microprocessor Report*, MicroDesign Resources, pp. 22-25 (March 28, 1994).
	AO	2	Gwennap, L., "Digital, MIPS Add Multimedia Extensions," *Microprocessor Report*, MicroDesign Resources, Vol. 10, No. 15, pp. 1-5 (November 18, 1996).
	AP	2	Gwennap, L., "New Multimedia Chips to Enter the Fray," *Microprocessor Report*, MicroDesign Resources, Vol. 10, No. 13, 1 page (October 1996).
	AQ	2	Hansen, C., "Architecture of a Broadband Mediaprocessor," *COMPCON 96*, IEE, pp. 1-8 (February 25-29, 1996).
	AR	2	*DSP56002 24-Bit Digital Signal Processor User's Manual*, Motorola, Inc., 386 pages (1995). (Bates Numbers L07277-L07662).

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